

Sangeetha R.G's education includes a Bachelor of Engineering in Electronics and Communication from Bharathidasan University, Trichy. She completed her Master's in Computer and Communication Engineering, Anna University, Chennai. Her doctoral studies were conducted at Indian Institute of Technology Delhi, where she completed her PhD in Optical Networks. Her main research interests are in Fiber Optic Communications, Optical Networks, and Free Space Communication. Her research credentials include an U.S patent, few Indian patents and a couple of intellectual awards. She has also published her research findings in more than 35 journals and peer reviewed conferences. Currently she has completed a funded project, titled "Test Bed for Hardware Implementation of All Optical Bi-Directional Switching Node" sponsored by Department of Science and Technology, Government of India under the Fast-Track Young Scientist Scheme. Currently, She is guiding 4 research scholars and M.Tech students. She is currently working in the position of Associate Professor in School of Electronics Engineering, VIT Chennai Campus. She is a senior member of IEEE, OSA and life member of OSI societies.